

Application No.: 09/752,123

Docket No.: JCLA6706

**In The Claims:****Claim 1. (Canceled)**

2. (Currently Amended) ~~[The apparatus for coprocessor data access control as claimed in claim 1]~~ **An apparatus for coprocessor data access control, comprising**

**a central processing unit, for executing central processing unit instructions to perform data processing, wherein the central processing unit instructions includes a plurality of coprocessor memory access instructions;**

**a memory unit, coupled to the central processing unit, for storing data words; and**

**a coprocessor, coupled to the central processing unit and the memory unit, for accessing and processing the data words stored in the memory unit by one of addressing modes under control of the coprocessor memory access instructions executed by the central processing unit, wherein**

**the coprocessor memory access instruction having an indicating field, and N data words are accessed to or from the memory unit by the coprocessor according to the value of the indicating field, wherein N is a value greater than or equal to 1,**

wherein the indicating field of the coprocessor memory access instruction includes a coprocessor number field, for storing information about a specific coprocessor to be activated.

**Claim 3. (Canceled)**

4. (Currently Amended) The apparatus for coprocessor data access control as claimed in claim [1] 2, wherein the indicating field of the coprocessor memory access instruction includes ~~[a coprocessor number field and]~~ a coprocessor register field, wherein ~~[coprocessor number field is used for storing information about a specific coprocessor to be activated, and]~~ the coprocessor

Application No.: 09/752,123

Docket No.: JCLA6706

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register field is used for storing information about specific registers to be used in the data processing.

**Claim 5. (Canceled)**

6. (Currently Amended) ~~[The method for coprocessor data access control as claimed in claim 5;~~ A coprocessor data access control method, comprising the steps of:

providing an instruction having an indicating field; and

accessing N data words to or from a memory unit by a specified coprocessor according to the value in the coprocessor indicating field, wherein N is a value greater than or equal to 1,

wherein the indicating field of the coprocessor memory access instruction includes a coprocessor number field, for storing information about a specific coprocessor to be activated.

**Claim 7. (Canceled)**

8. (Currently Amended) The method for coprocessor data access control as claimed in claim [5] 6, wherein the indicating field of the coprocessor memory access instruction includes ~~[a coprocessor number field and]~~ a coprocessor register field, wherein ~~[coprocessor number field is used for storing information about a specific coprocessor to be activated, and]~~ the coprocessor register field is used for storing information about specific registers to be used in the data processing.

9. (Currently Amended) An instruction format recorded on a computer readable medium for a coprocessor data access control, wherein the instruction format includes an indicating field so that while the instruction format is read by the computer, [and] a particular coprocessor [to] being used and the number of data words [to] being accessed to or from a memory unit is determined by the value of in the indicating field.

Application No.: 09/752,123

Docket No.: JCLA6706

10. (Original) The instruction format of claim 9, wherein the indicating field of the instruction format is a coprocessor number field.

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11. (Original) The instruction format of claim 9, wherein the indicating field of the instruction format is a coprocessor register field.

12. (Original) The instruction format of claim 9, wherein the indicating field of the coprocessor memory access instruction includes a coprocessor number field and a coprocessor register field, wherein coprocessor number field is used for storing information about a specific coprocessor to be activated, and the coprocessor register field is used for storing information about specific registers to be used in the data processing.